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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

none BO

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2003-051201 02/27/2003

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Examiner's Signature: [Signature] Initials: [Initials] Met after allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 22	INDEPENDENT CLAIMS 5
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## TITLE

Processor, system LSI circuit, method of designing same, and recording medium having said method recorded thereon

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